

Amendments to the Specification

Please add the following new paragraph after the paragraph ending on line 13 of page 5.

Figure 5 is a schematic drawing of a distributed instruction cache according to the present invention.

Should any outstanding issues remain, the Examiner is encouraged to contact the undersigned at (650)236-4868 so that any such issues can be expeditiously resolved.

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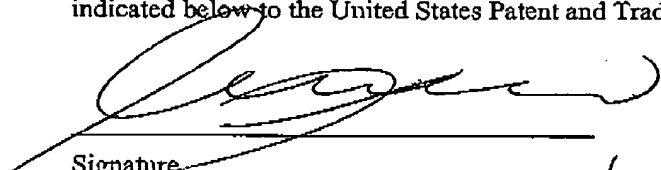
Respectfully submitted,

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By 

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I hereby certify that this correspondence is being transmitted via facsimile on the date indicated below to the United States Patent and Trademark Office 571/273-8300.


Signature

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Date: 11/2/05